

1 **CLAIM LISTING**

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3 1. (Previously presented) Each of a plurality of coupling circuits for a Serial
4 ATA storage device, comprising:

5 a first Serial ATA controller-side transceiver receiving a first Serial ATA
6 communication path;

7 a second Serial ATA controller-side transceiver receiving a second Serial ATA
8 communication path;

9 a Serial ATA storage device-side transceiver;

10 coupling circuit switches selectively coupling either the first Serial ATA controller-
11 side transceiver or the second Serial ATA controller-side transceiver to the Serial ATA
12 storage device-side transceiver; and

13 a microcontroller adapted to control the coupling circuit switches.
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15 2. (Previously presented) Each of the plurality of coupling circuits of claim 1,
16 further comprising an out of band squelch control component for activating the first
17 Serial ATA controller-side transceiver receiving a first Serial ATA communication path,
18 the second Serial ATA controller-side transceiver receiving a second Serial ATA
19 communication path, and the Serial ATA storage device-side transceiver.
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21 3. (Previously presented) Each of the plurality of coupling circuits of claim 1,
22 wherein the microcontroller includes a processor coupled to a power switch and coupled
23 to the coupling circuit switches.
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25 4. (Previously presented) Each of the plurality of coupling circuits of claim 1,
26 wherein the microcontroller includes a processor coupled to a set of D flip-flops that are
27 coupled to a power switch and coupled to the coupling circuit switches.
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1 5. (Previously presented) Each of the plurality of coupling circuits of claim 1,
2 wherein the microcontroller is programmed to as follows:

3 switch the coupling circuit to a first storage controller;
4 switch the coupling circuit to a second storage controller;
5 power up the Serial ATA storage device; and
6 power down the Serial ATA storage device.

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8 6. (Previously presented) Each of the plurality of coupling circuits of claim 5,
9 wherein the microcontroller is further programmed to as follows:

10 write data to a memory;
11 read data from the memory; and
12 read the status of the coupling circuit.

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14 7. (Previously presented) Each of the plurality of coupling circuits of claim 6,
15 wherein the status includes information on whether the Serial ATA storage device is
16 coupled to the first Serial ATA controller-side transceiver or the second Serial ATA
17 controller-side transceiver, the Serial ATA storage device is powered up or down, the
18 communication status, and/or the board revision and code revision levels of the
19 coupling circuit.

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21 Claims 8-21 (Withdrawn)

22 22. (Previously presented) Each of a plurality of coupling circuits for a storage
23 device, comprising:

24 a first controller-side transceiver receiving a first communication path;
25 a second controller-side transceiver receiving a second communication path;
26 a storage device-side transceiver;
27 coupling circuit switches selectively coupling either the first controller-side
28 transceiver or the second controller-side transceiver to the storage device-side
29 transceiver; and
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1 a microcontroller adapted to control the coupling circuit switches and control the
2 power to the storage device.

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4 23. (Previously presented) Each of a plurality of coupling circuits for a Serial
5 ATA storage device, comprising:

- 6 means for receiving a first Serial ATA communication path;
- 7 means for receiving a second Serial ATA communication path;
- 8 means for coupling either the first Serial ATA communication path or the second
- 9 Serial ATA communication path to the Serial ATA storage device; and
- 10 a microcontroller adapted to control the coupling circuit switches.